KAKATIYA INSTITUTE OF TECHNOLOGY & SCIENCE
(An Autonomous Institute under Kakatiya University, Warangal)
Opp : Yerragattugutta, Bheemaram (V), Hasanparthy (Mandal), WARANGAL - 506 015, Telangana State, INDIA.
काकतीय प्रौद्योगिकी एवं विज्ञान संस्थान, वरंगल - ५०६ ०१५ तेलंगाना राज्य, भारत
కాకతీయ సాంకేతిక విజ్ఞాన శాస్త్ర విద్యాలయం, వరంగల్ - 506 015. తెలంగాణా రాష్ట్రం, భారతదేశము
Office of the Controller of Examinations

for Supplementary Students

2019 to 2021 Admitted Batches

No. ESE Schedule Supply/B.Tech. III-Sem (URR18)/2026/279

Date: 12-11-2026

END SEMESTER EXAMINATIONS (ESE) FOR B.TECH. III-SEMESTER (URR18) (SUPPLEMENTARY) NOVEMBER/DECEMBER 2026

TIME – TABLE

Examination Timings: FN: 10.00 am to 01.00 pm

Date & Day of Examination	CE	ME	CSE	EEE	ECE	IT	CSE(N)	ECI	CSE (IoT)	CSE (AIML)
18.11.2026 Wednesday	Engineering Mathematics-III	Engineering Mathematics - III	Engineering Mathematics-III	Engineering Mathematics-III	Engineering Mathematics-III	Engineering Mathematics-III	Engineering Mathematics-III	Engineering Mathematics-III	Engineering Mathematics-III	Engineering Mathematics-III
20.11.2026 Friday	**OE-I** Strength of Materials	Mechanics of Solids	Object Oriented Programming through JAVA	Electrical Measurements & Instrumentation	**OE-I** Object Oriented Programming	Operating Systems	Object Oriented Programming through JAVA	**OE-I** Object Oriented Programming	Object Oriented Programming through JAVA	Object Oriented Programming through JAVA
25.11.2026 Wednesday	Fluid Mechanics	Material Science and Metallurgy	Discrete Mathematics	Electromagnetic Fields	Signals & Systems	Discrete Mathematics	Database Management Systems / Operating Systems	Electronic Measurements and Sensors	Fundamentals of Internet of Things	Operating Systems
27.11.2026 Friday	Surveying	Engineering Thermodynamics	Computer Architecture and Organization	Network Theory	Network Analysis	Computer Architecture and Organization	Computer Architecture and Organization	Signals Systems and Random Processes	Computer Networks	Computer Organization and Architecture
30.11.2026 Monday	Construction Materials	Machine Drawing	Advanced Data Structures	Electronic Devices and Circuits	Analog Circuits – I	Advanced Data Structures / Object Oriented Programming through C++	Advanced Data Structures	Electronic Devices and Applications	Advanced Data Structures	Advanced Data Structures
02.12.2026 Wednesday	--	--	Digital Electronics	--	Switching Theory & Logic Design	Switching Theory and Logic Design	Digital Electronics	Digital Circuits and Logic Design	Digital Electronics	Formal Languages and Automata Theory
04.12.2026 Friday	--	i) Essence of Indian Traditional Knowledge ii) Yoga and Positive Psychology for Managing Career and Life	i) Essence of Indian Traditional Knowledge ii) Yoga and Positive Psychology for Managing Career and Life	--	--	i) Essence of Indian Traditional Knowledge ii) Yoga and Positive Psychology for Managing Career and Life	i) Essence of Indian Traditional Knowledge ii) Yoga and Positive Psychology for Managing Career and Life	--	i) Essence of Indian Traditional Knowledge ii) Yoga and Positive Psychology for Managing Career and Life	--

Candidate should occupy his/her seat at least **10 minutes before** the commencement of the examination.

To: Exam Branch Notice Boards

Copy to: 1. Principal *for favour of information* 2.Dean, Academic Affairs 3. HOD's- with a request to display in the Dept Notice Boards & share in the Class WhatsApp groups
4. Training & Placement Officer 5.Web Team- with a request to display on the kitsw.ac.in/examinations/Examinations_schedules.html

Controller of Examinations